IN THE CLAIMS:

Please amend the claims as follows:

1.   (Previously Presented) A data processing method using a multiplicity of processors which operate in parallel and to which a respective command for data processing is supplied simultaneously, at least one of the processors being alternatively supplied with a program flow control command or a condition command, the supplying of the condition command deactivating the parallel execution of a further command in at least one of the further processors.

2.   (Previously Presented) The data processing method as claimed in claim 1, wherein the supplying of the condition command has the effect that the computational result of one of the processors is not written back into a target register which is provided.

3.   (Currently Amended) The data processing method as claimed in claim 1, wherein the supplying of the condition command has the effect that an address for the parallel executing of the further command is not calculated.

4.   (Previously Presented) The data processing method as claimed in claim 1, wherein the supplying of the condition command has the effect that a command is not executed by the at least one of the further processors.

5.   (Previously Presented) The data processing method as claimed in claim 1, wherein the further commands comprise arithmetic computational commands and/or move commands.

6.   (Previously Presented) The data processing method as claimed in claim 1, wherein the condition which is associated with the condition command is the same for all of the further processors.

7.   (Previously Presented) The data processing method as claimed in claim 1, wherein the condition which is associated with the condition command is different from all the further processors.